

Atty Docket No.: IDF 1764
(4000-06700)

Patent

AMENDMENTS TO THE SPECIFICATION

- (1) Please replace paragraph [0052] with the following paragraph:

[0052] SDRAM [[54]] 53 is normally considered to be volatile memory. As a result, it would be expected that the trial run message stored in step 304 would not survive the reboot process. However, we have found that an automatic or soft reboot normally occurs in less than one second and that data stored in SDRAM survives for at least that long without the normal refresh cycle. In the unusual case, where for some reason the trial run message does not survive, it is a failsafe occurrence. The system will simply restart with the "old" binary file which remains flagged in flash memory 54 as the currently active binary file.